FEE Enclosed: \$ -NONE-Please charge any further fee to Dep. Acct. 19-3700 OFFICE 03/200 U.S. PTO 04/229628

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Branch

In re PATENT APPLICATION of

Applicants : Yasutaka SAKAINO et al.

Appln. No. : To be Assigned

Filed : January 13, 1999

For : SEMICONDUCTOR INTEGRATED

CIRCUIT

Atty. Dkt. : OKI 4646.01

INFORMATION

DISCLOSURE

STATEMENT

January 13, 1999

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

This is an Information Disclosure Statement submitted under 37 CFR 1.97 ff within the time specified in 37 CFR 1.97(b).

Attached is a copy of a publication considered relevant to the above-identified application.

A Form PTO-1449 listing the publication is attached.

In view of the above, the requirements set forth in Section A(3) of the Commissioner's Notice of April 20, 1992, 1138 OG 37 ff (May 19, 1992), have been complied with.

As all requirements of 37 CFR 1.97 ff and all official quidelines pertaining to Information Disclosure Statements,

ave been complied with, it is respectfully requested that the Examiner consider the reference and make it of record.

Respectfully submitted,

Robert J. Frank

Registration No. 19,112

VENABLE

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RJF:jh